Substitute for form 1449A/PTO	Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT *(use as many sheets as necessary)*	Application Number	Not yet assigned
	Filing Date	Herewith
	First Named Inventor	MATHENY, David L.
	Group Art Unit	Not yet assigned
	Examiner Name	Not yet assigned
Sheet 1 of 1	INTEL Docket Number	P18484

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
/MR/	1	Wade, Roy and Neshati, Ramin "The PC-AT Boot Process and Option ROMs", Intel® DeveloperUPDATEMagazine, Sept/Oct 1999, pages 1-11.	
/MR/	2	"BIOS Boot Specification", Version 1.01, January 11, 1996, Compaq Computer Corporation, Phoenix Technologies Ltd., Intel Corporation. Pages 1-46.	

Examiner Signature	/Mohammed Rehman/	Date Considered	04/05/2007

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1] Unique citation designation number. [2] Applicant is to place a check mark here if English language Translation is attached.